

## Changes To The Claims

Please add claims 17-21 as follows:

17. A programmable logic device, comprising:  
a global switch matrix (GSM); and  
a plurality of logic-containing segments operatively coupled to the GSM,  
wherein each of the plurality of segments has within it and dedicated for  
intra-segment communications at least as many longlines as there are  
macrocells and I/O pads in the segment.
18. The programmable logic device of claim 17, wherein each of the plurality  
of segments has within it and dedicated for inter-segment communications at  
least as many longlines as there are macrocells and I/O pads in the segment.
19. The programmable logic device of claim 17, wherein the GSM has at least  
as many longlines for inter-segment communications as do at least two  
segments.
20. A programmable logic device, comprising:  
a global switch matrix (GSM); and  
a plurality of logic-containing segments operatively coupled to the GSM,  
wherein each of the plurality of segments has within it and dedicated for  
inter-segment communications at least as many longlines as there are  
macrocells and I/O pads in the segment.
21. A programmable logic device, comprising:  
a global switch matrix (GSM); and  
a plurality of logic-containing segments operatively coupled to the GSM,  
wherein the GSM has at least as many longlines for inter-segment  
communications as do at least two segments.

### **Status Of Claims And Support For Claim Changes**

Claims 1-21 are pending. Claims 1-16 are unamended. Claims 17-21 have been added.

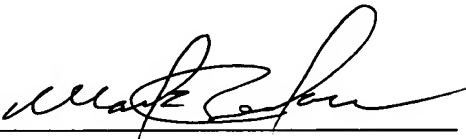
Support for claim 17 is found in at least claim 15; in col. 9, lines 53 to 65; in FIGs. 2 and 5; and in col. 15, lines 22-50.

Support for claims 18 and 20 is found in at least col. 9, line 66 to col. 10, line 5; in FIGs. 2 and 5; and in col. 15, lines 61-65.

Support for claims 19 and 21 is found in at least col. 10, lines 5-7; in FIGs. 2 and 5; and in col. 18, lines 54-57.

Respectfully submitted,

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